

<b>Notice of References Cited</b>	Application/Control No. 10/064,333	Applicant(s)/Patent Under Reexamination GREENBLAT ET AL.	
	Examiner Derrick W. Ferris	Art Unit 2663	Page 1 of 1

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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	DUNLOP et al. "Alternative approach to ASIC design methodology based on reconfigurable logic devices". IEEE. April 1992. Pages 217-221.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.